

S/N 09/834,751

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sergey A. Velichko et al.

Examiner: Manuel Barbee

Serial No.: 09/834,751

Group Art Unit: 2857

Filed: April 13, 2001

Docket: 303.750US1

Confirmation No.: 4280

Title: CONCURRENT CONTROL OF SEMICONDUCTOR PARAMETRIC
TESTING

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants hereby authorize the Commissoner to charge the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p), to Deposit Account No. 19-0743. Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

Applicant brings to the attention of the Examiner the following Office Actions and responses associated with one or both of the inventors of the instant application and/or the assignee of the instant application. This material is available and accessible at the United States Patent and Trademark Office.

DYNAMIC CREATION AND MODIFICATION OF WAFER TEST MAPS DURING WAFER TESTING

Application No. 11/265,897 (Attorney Docket No. 303.855us2) Response filed 1/29/2008 to Final Office Action mailed 11/19/2007, 8 pages

Application Serial No. 11/265,897 (Atty. Ref. 303.855US2), Non-Final Office Action mailed 02/25/2008, 11 Pgs

Application Serial No. 11/265,897 (Atty Ref 303.855us2) Response filed 5/27/2008 to Non-Final Office Action mailed 02/25/2008, 7 pages

DYNAMICALLY ADAPTABLE SEMICONDUCTOR PARAMETRIC TESTING

Application Serial No. 11/342,425 (Atty. Ref. 303.821US4) Notice of Allowance mailed 01-23-08, 16 pgs

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications, and Non-Published Applications identifiable by USPTO Serial Number, are no longer required to be provided to the Office. Notification of this change to this effect was provided in the United States Patent and Trademark Office OG Notice dated October 19, 2004. Thus, Applicant has not included copies of any US Patents or US Patent Applications identifiable by serial number that may be cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

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Date Aug 8 '08 By JMD
John M. Dahl
Reg. No. 44,639

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 8/15 day of August, 2008.

Name Amy Moriarty

Signature GJS